

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	21	essick-raymond\$.in. or moat-kent\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:01
L2	19245	tlb or (address near translation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:02
L3	1	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:02
L4	2728	711/202.ccls. or 711/206-207.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:03
L5	2507	710/305-306.ccls. 710/314.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:03
L6	2507	710/305-306.ccls. 710/314.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:03
L7	4568	(physical adj1 address) same (virtual adj1 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:04
L8	2206	2 same 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:04

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L9	5214	4 5 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:04
L10	586	8 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:04
L11	1795	(tlb (address near5 translation)) same (bus with (interface bridge controller arbiter))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:14
L12	1021	((external peripheral input/output i/o) with device) same (virtual near3 address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:05
L13	33	10 and 11 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:05
L14	29	13 and (@ad<="20030829" or @rlad<="20030823")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:43
L15	68	11 and 12 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:42
L16	23	protocol and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:50

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L17	21	16 and (@ad<="20030829" or @rlad<="20030823")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:14
L18	11	(bus near5 protocol) and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 11:59
L19	48	advanced adj1 micro-controller adj1 bus adj1 architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:00
L20	1508	(advanced adj1 micro-controller adj1 bus adj1 architecture) AMBA AHB (advanced adj1 high adj1 performance adj1 bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:04
L21	2	15 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:04
L22	29	20 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:14
L23	8	22 and (@ad<="20030829" or @rlad<="20030823")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:27
L24	0	(8 and 12 and (bus with protocol) and (processor microprocessor)and memory).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 12:30
L25	0	(8 and 12 and (bus with protocol) and (processor microprocessor)and memory) .clm.	US-PGPUB	OR	ON	2006/07/24 12:31

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L26	0	(8 and 12 and 20 and (processor microprocessor)and memory) .clm.	US-PGPUB	OR	ON	2006/07/24 12:31
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